

## KF556C36BWEAK2-64

64GB (32GB 4G x 64-Bit x 2 pcs.)

DDR5-5600 CL36 288-Pin DIMM Kit



## DEFAULT SPECIFICATIONS

|                                                  |                   |
|--------------------------------------------------|-------------------|
| CL(IDD)                                          | 40 cycles         |
| Row Cycle Time (tRCmin)                          | 48ns(min.)        |
| Refresh to Active/Refresh Command Time (tRFCmin) | 295ns(min.)       |
| Row Active Time (tRASmin)                        | 32ns(min.)        |
| UL Rating                                        | 94 V - 0          |
| Operating Temperature                            | 0° C to +85° C    |
| Storage Temperature                              | -55° C to +100° C |

## DESCRIPTION

Kingston FURY KF556C36BWEAK2-64 is a kit of two 4G x 64-bit (32GB) DDR5-5600 CL36 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. Each module kit supports AMD® EXPO v1.0 and Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 64GB. Each module has been tested to run at DDR5-5600 at a low latency timing of 36-38-38 at 1.25V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

## DEFAULT FEATURES

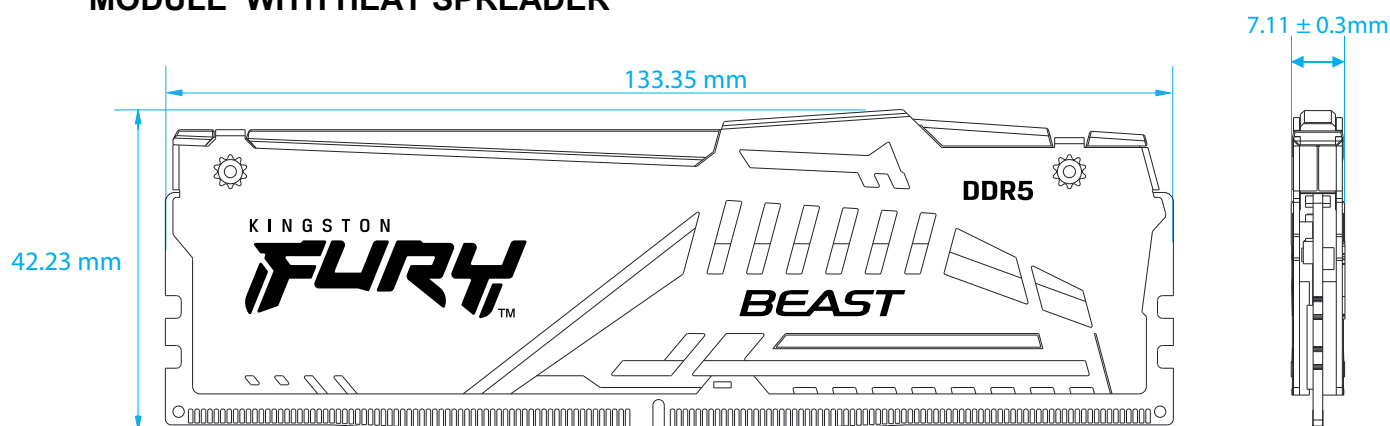
- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.66" (42.23mm), w/heatsink

## FACTORY TIMING PARAMETERS

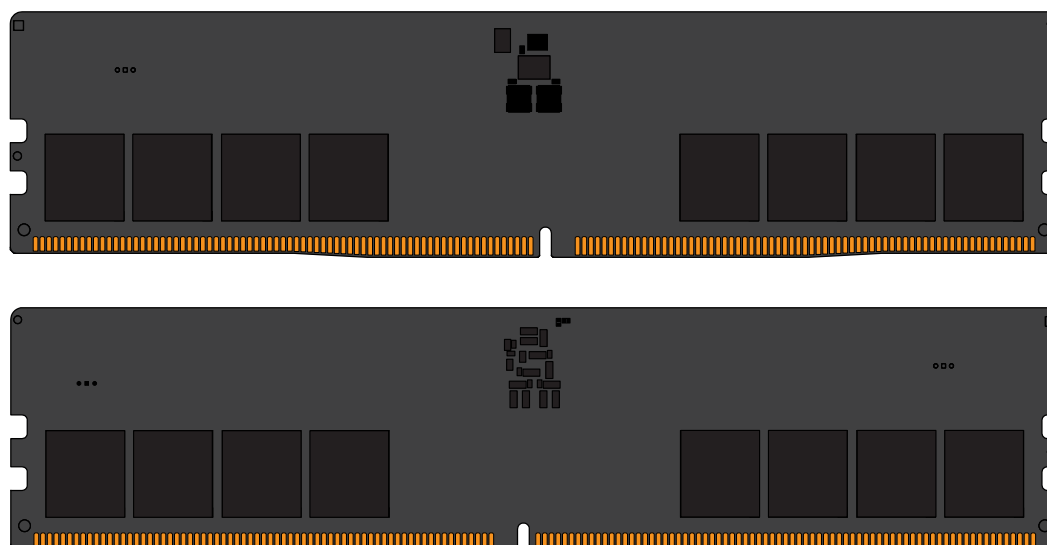
- Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
- EXPO Profile #0: DDR5-5600 CL36-38-38 @1.25V
- EXPO Profile #1: DDR5-5200 CL40-40-40 @1.25V
- XMP Profile #1: DDR5-5600 CL36-38-38 @1.25V
- XMP Profile #2: DDR5-5200 CL40-40-40 @1.25V

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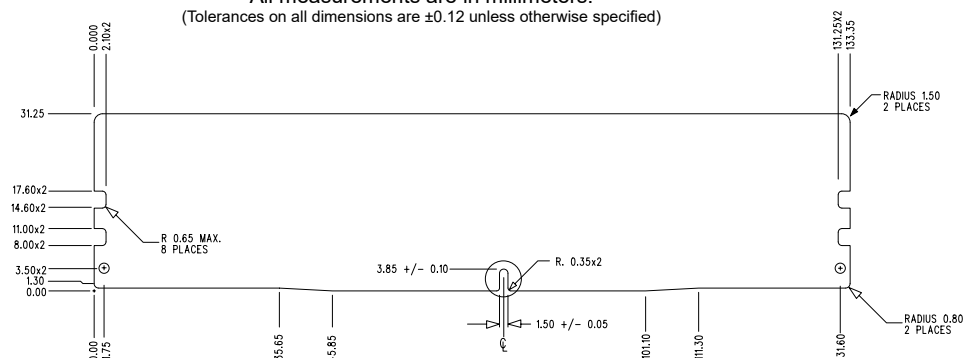
## MODULE WITH HEAT SPREADER



## MODULE DIMENSIONS



All measurements are in millimeters.  
(Tolerances on all dimensions are  $\pm 0.12$  unless otherwise specified)



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